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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:  John W. Rapp, Larry Jackson, Mark Jones, Troy Cherasaro  
Title: PROGRAMMABLE CIRCUIT AND RELATED COMPUTING MACHINE AND METHOD  
Serial Number: 10/684,057  
Filing Date: October 9, 2003  
Examiner/Unit: / 2818  
Attorney Docket No.: 1934-14-3

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Signature

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In compliance with 37 CFR § 1.56 and § 1.97, Applicants acting by and through the attorney of record, hereby discloses patents and other documents listed on the attached Form PTO-1449 which may be considered relevant to the examination of the subject application.

Copies of the cited references are enclosed.

**Certification Under Section 1.97(e)**

Several items of information contained in this Information Disclosure Statement was first cited in any communication from a foreign Patent Office in a counterpart foreign application not more than three months prior to the filing of this statement.

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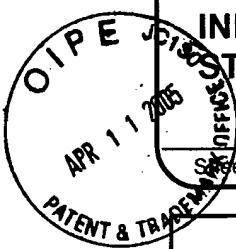
Respectfully submitted,

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(Use as many sheets as necessary)</i>		Application Number	10/684,057
		Filing Date	October 9, 2003
		First Named Inventor	John W. Rapp
		Art Unit	2818
		Examiner Name	
Sheet	1	of	2
		Attorney Docket Number	1934-14-3



## **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

*(Use as many sheets as necessary)*

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of 2

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<i>Application Number</i>	10/684,057
<i>Filing Date</i>	October 9, 2003
<i>First Named Inventor</i>	John W. Rapp
<i>Art Unit</i>	2818
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<i>Attorney Docket Number</i>	1934-14-3

## U.S. PATENT DOCUMENTS

## FOREIGN PATENT DOCUMENTS

Examiner Signature		Date Considered	
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**INFORMATION DISCLOSURE  
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Sheet

2

of

2

Attorney Docket Number

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Application Number	10/684,057
Filing Date	October 9, 2003
First Named Inventor	John W. Rapp
Art Unit	2818
Examiner Name	

1934-14-3

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		LECURIEUX-LAFAYETTE G: "Un Seul FPGA Dope Le Traitement D'Images", Electronique, CEP Communication, Paris, FR, no. 55, 1996, pp. 98, 101-103	
		VERMEULEN F. ET AL., "Flexible Hardware Acceleration for Multimedia Oriented Microprocessors", Micro-33. Proceedings of the 33 <sup>rd</sup> Annual ACM/IEEE International Symposium on Microarchitecture. Monterey, CA, Dec. 10-13, 2000, Proceedings of the Annual ACM/IEEE International Symposium on Microarchitecture, Los Alamitos, CA: IEEE Comp. Soc, US, 10 December 2000, pp. 171-177	
		International Search Report for PCT/US 03/34557, December 21, 2004	
		International Search Report for PCT/US 03/34559, January 7, 2005	

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